

OCT 14 2005

1374.43296X00

*Entered
w/ RCE
10/14/05*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. ICHINOSE, et al.
Serial No.: 10/733,377
Filed: DECEMBER 12, 2003
For: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE
Group AU: 2822
Examiner: Toniae M. Thomas
Confirm. No.: 8552

SUBMISSION UNDER 37 CFR 1.114
(AMENDMENT)

Mall Stop: RCE - FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 14, 2005

Sir:

Concurrently with the filing of a Request for Continued Examination (RCE)
Transmittal, and in response to the Office Action mailed July 14, 2005, please amend
the above-identified application as listed in the following, and as set forth on the
following pages:

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.

10/17/2005 TL0111 00000013 10733377

02 FC:1202

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